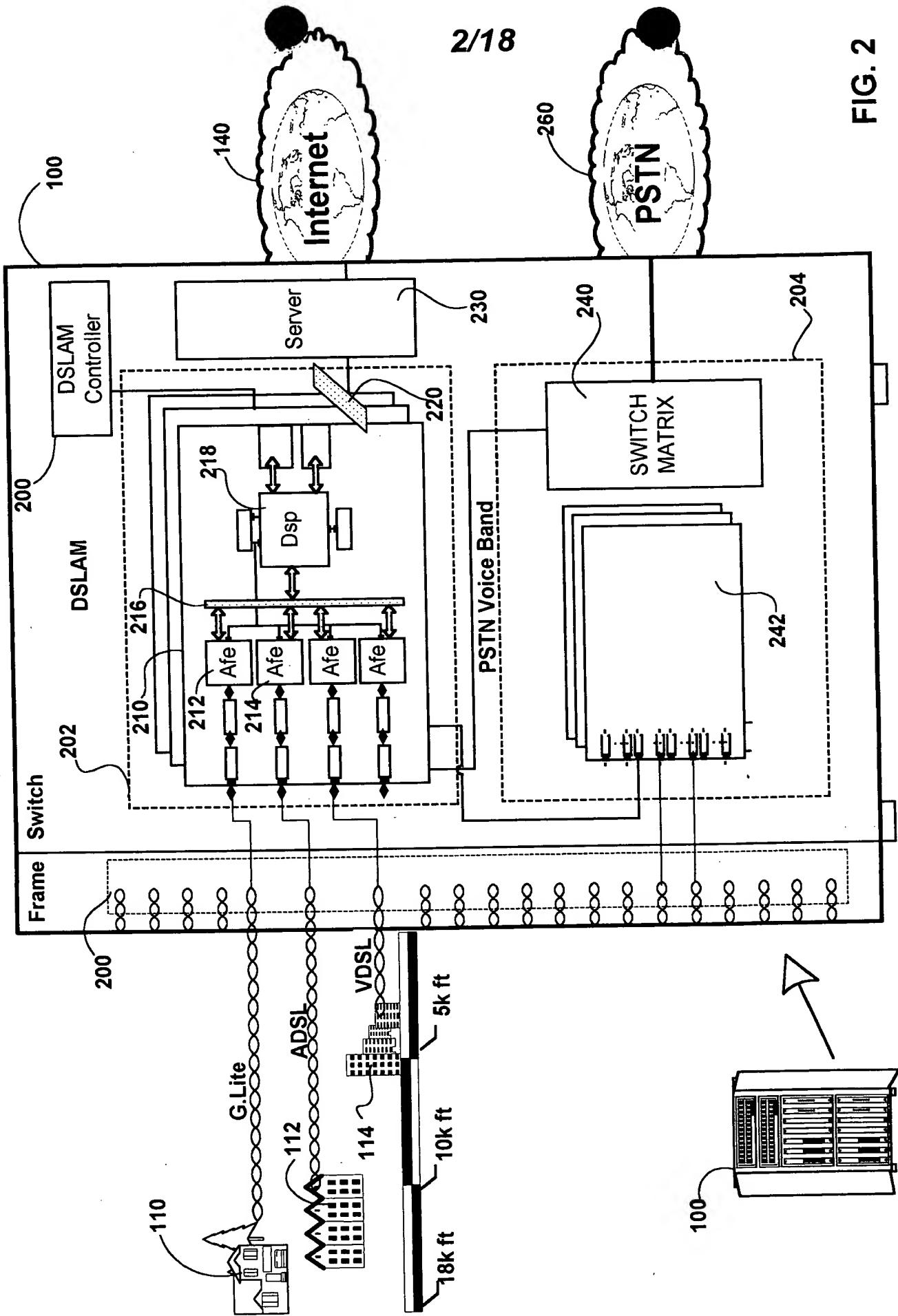
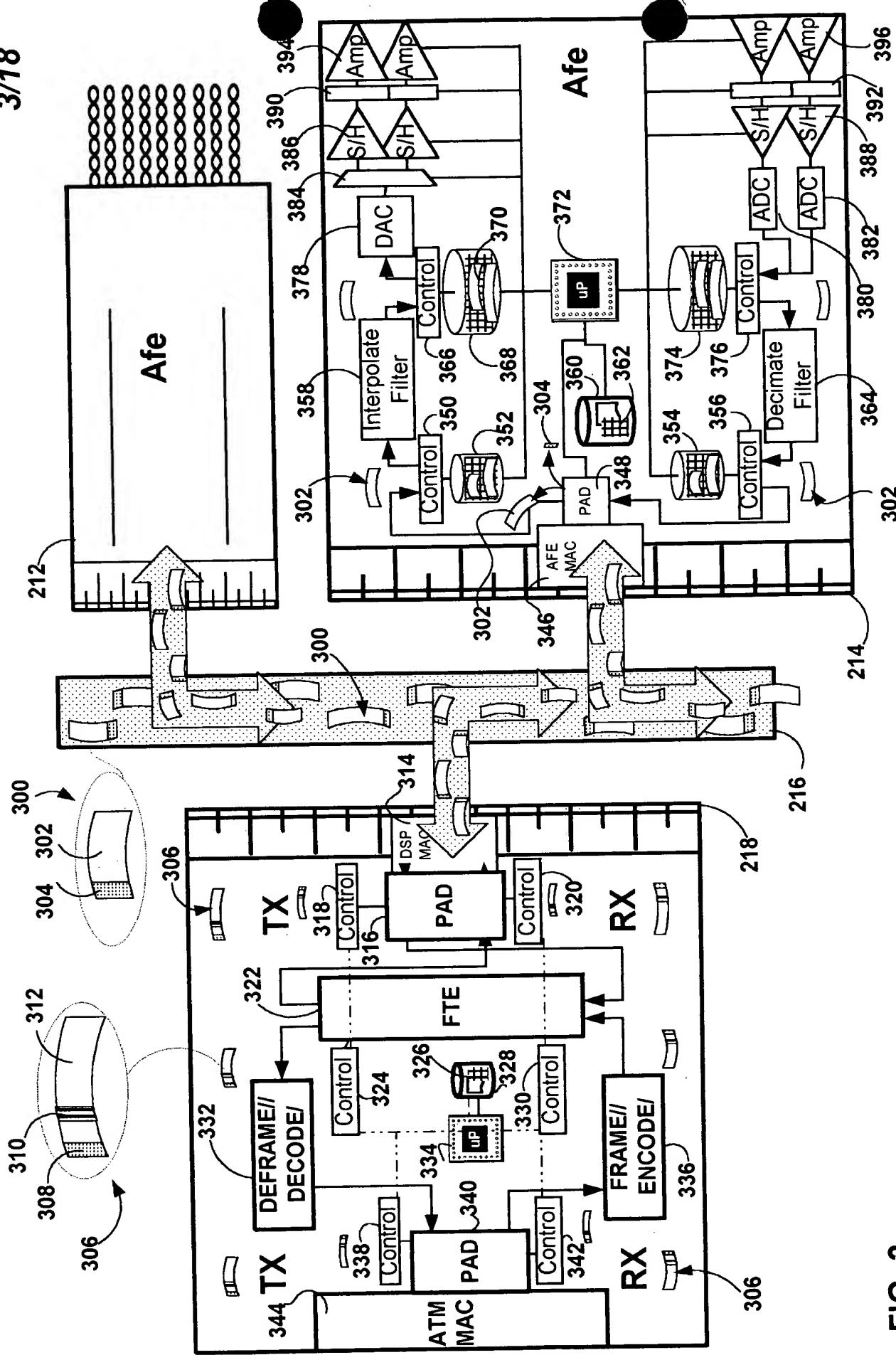


FIG. 1

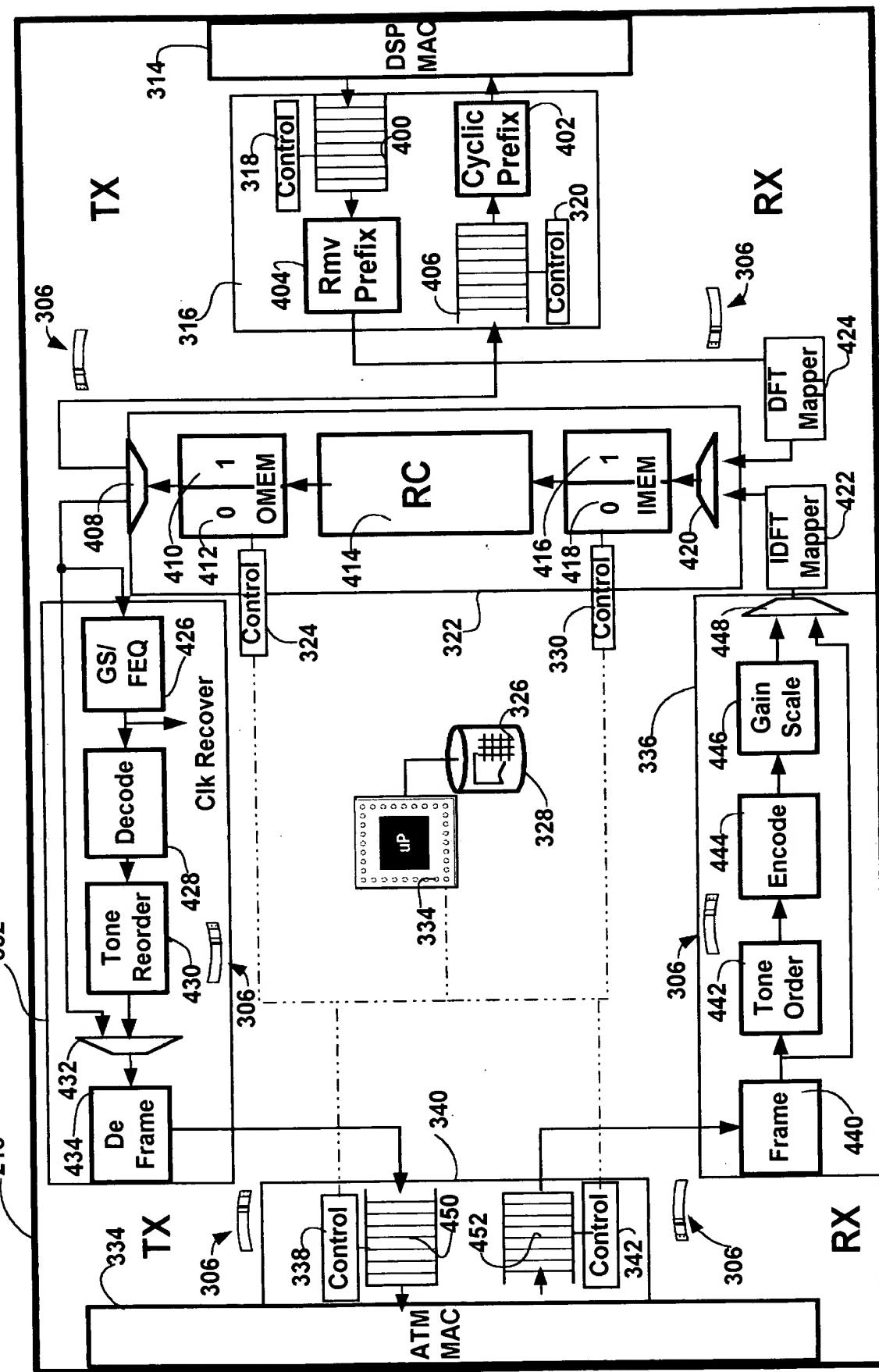
Packet Based distributed xDSL System



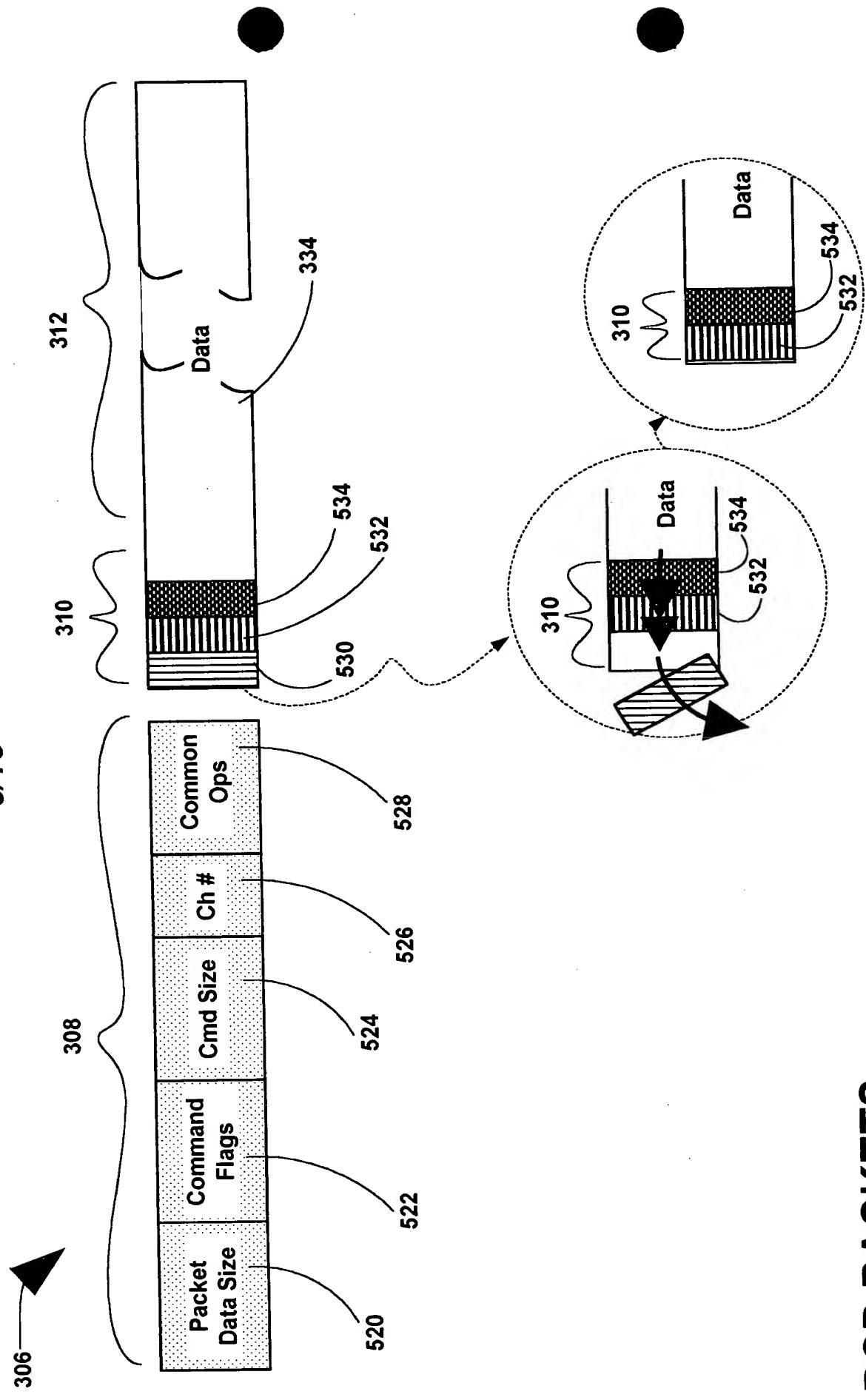


3
FIG.

FIG. 4



5/18



DSP PACKETS

FIG. 5

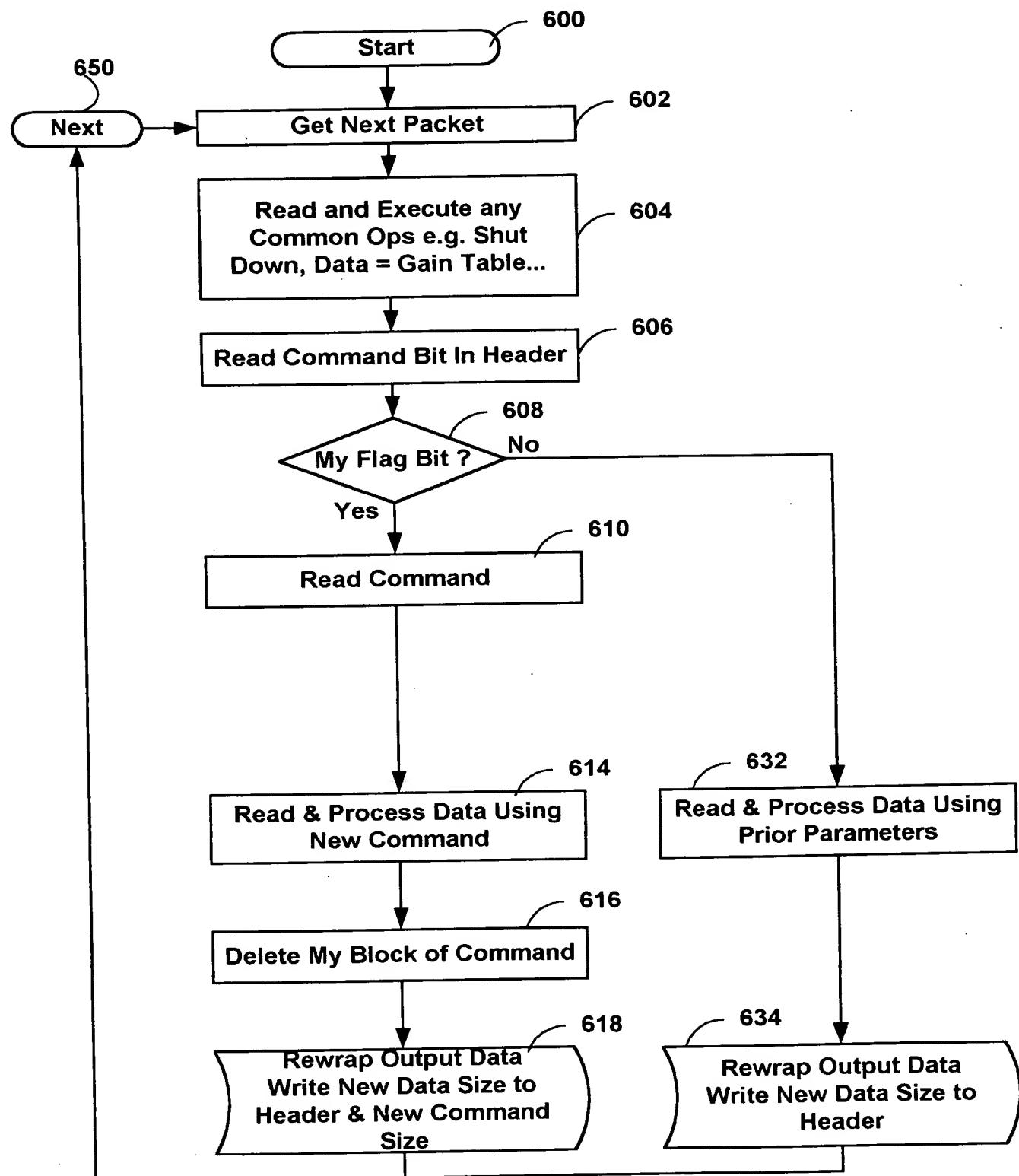


FIG. 6

7/18

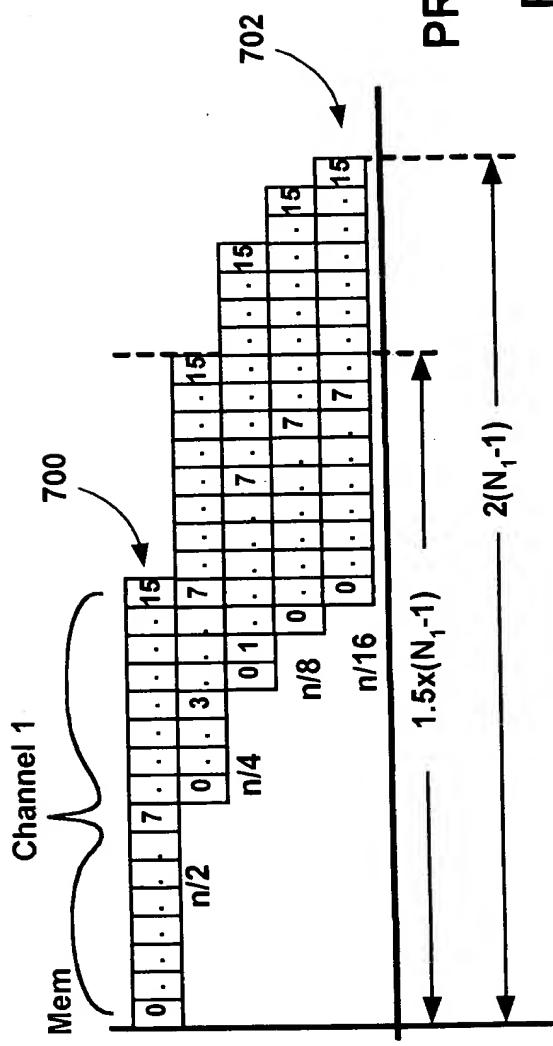


FIG. 7
PRIOR ART

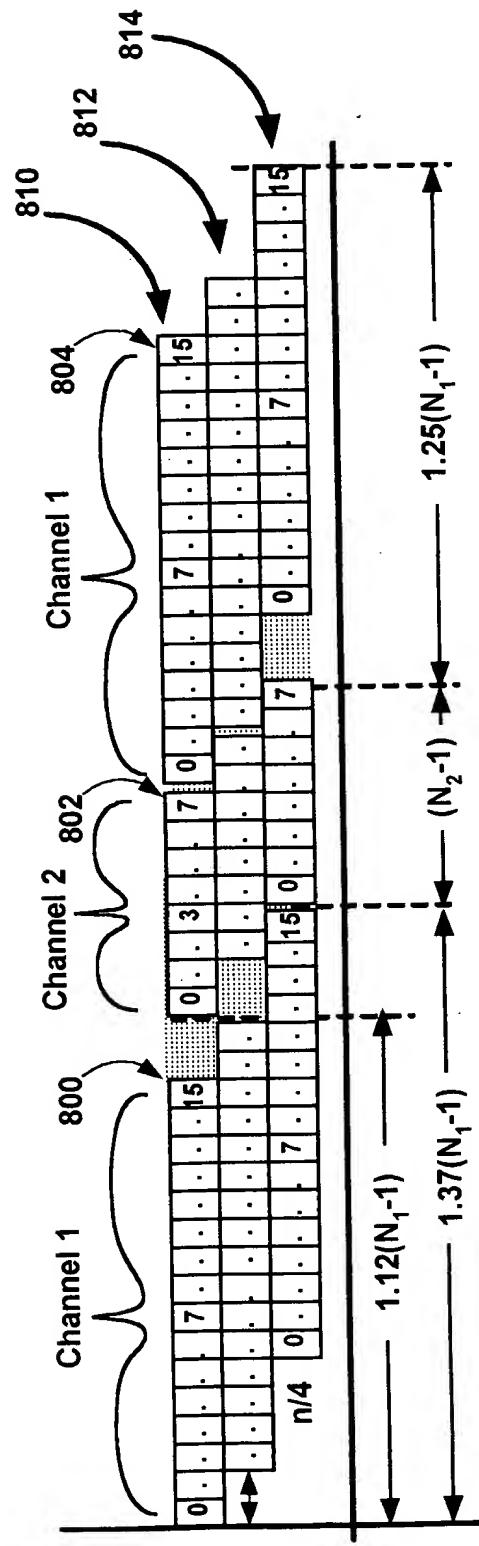


FIG. 8

FIG. 9A

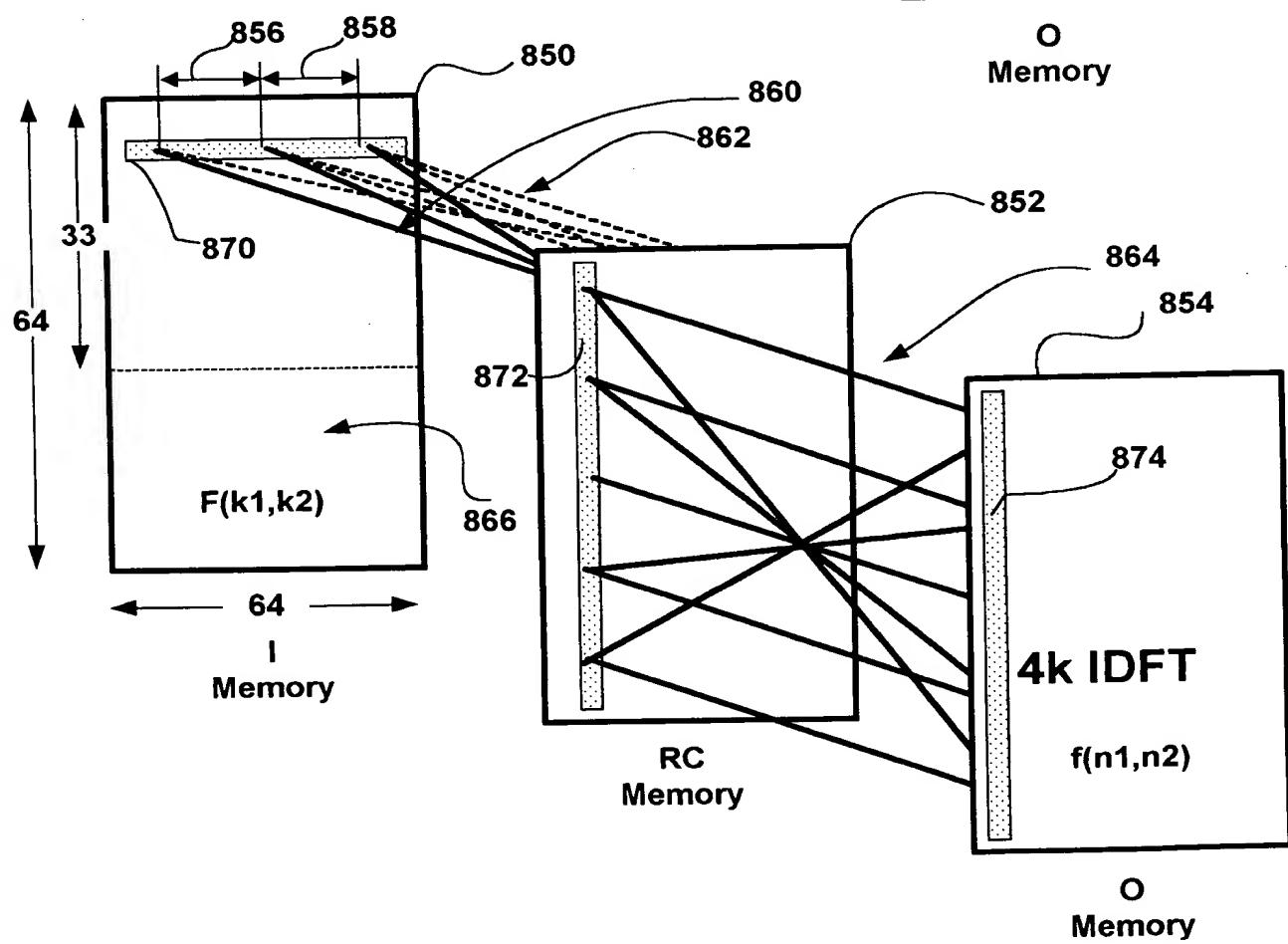
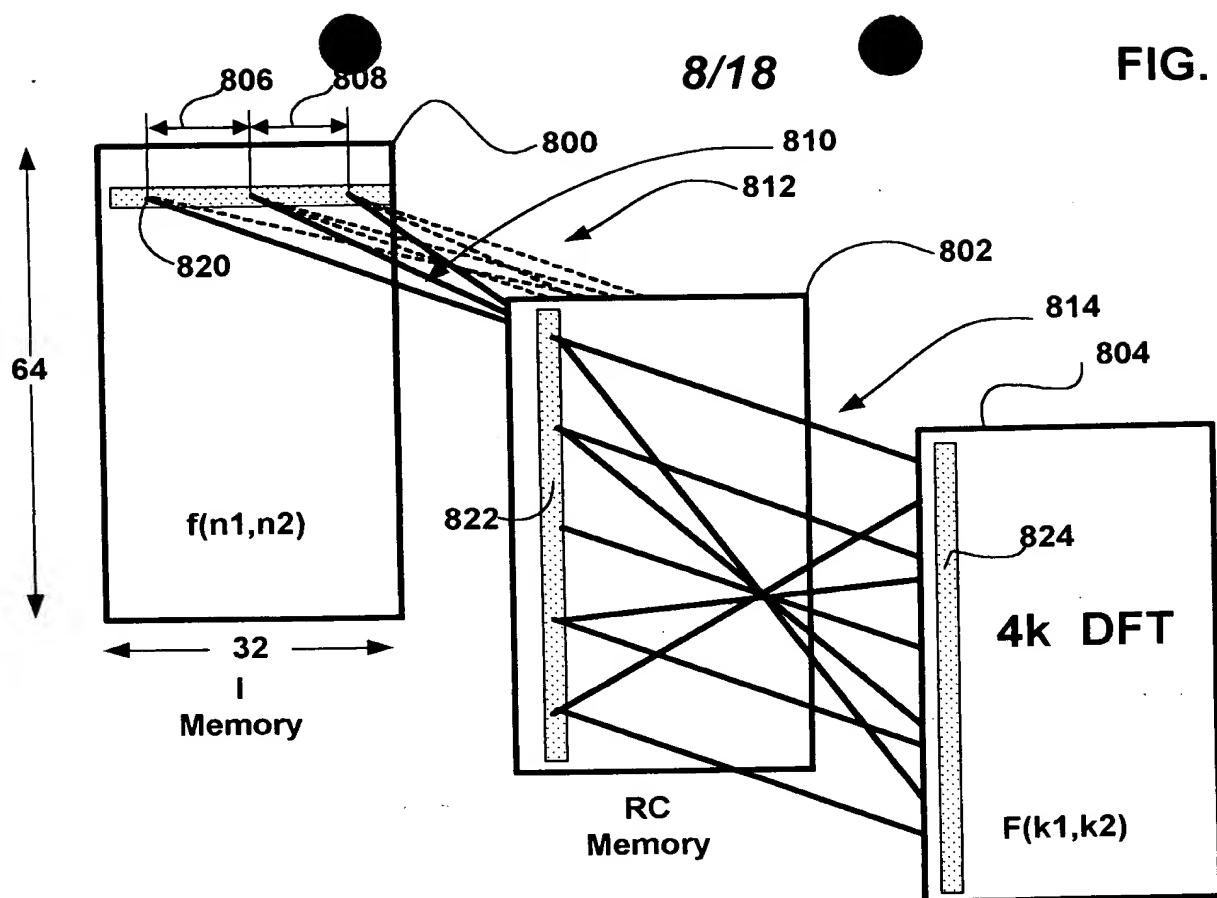


FIG. 9B

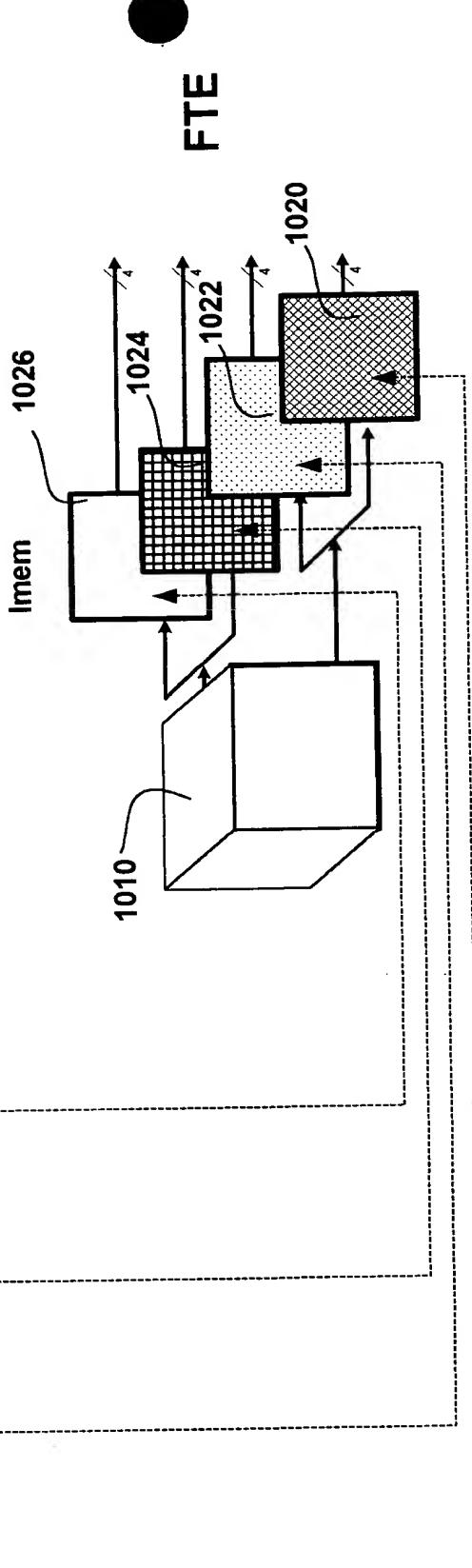
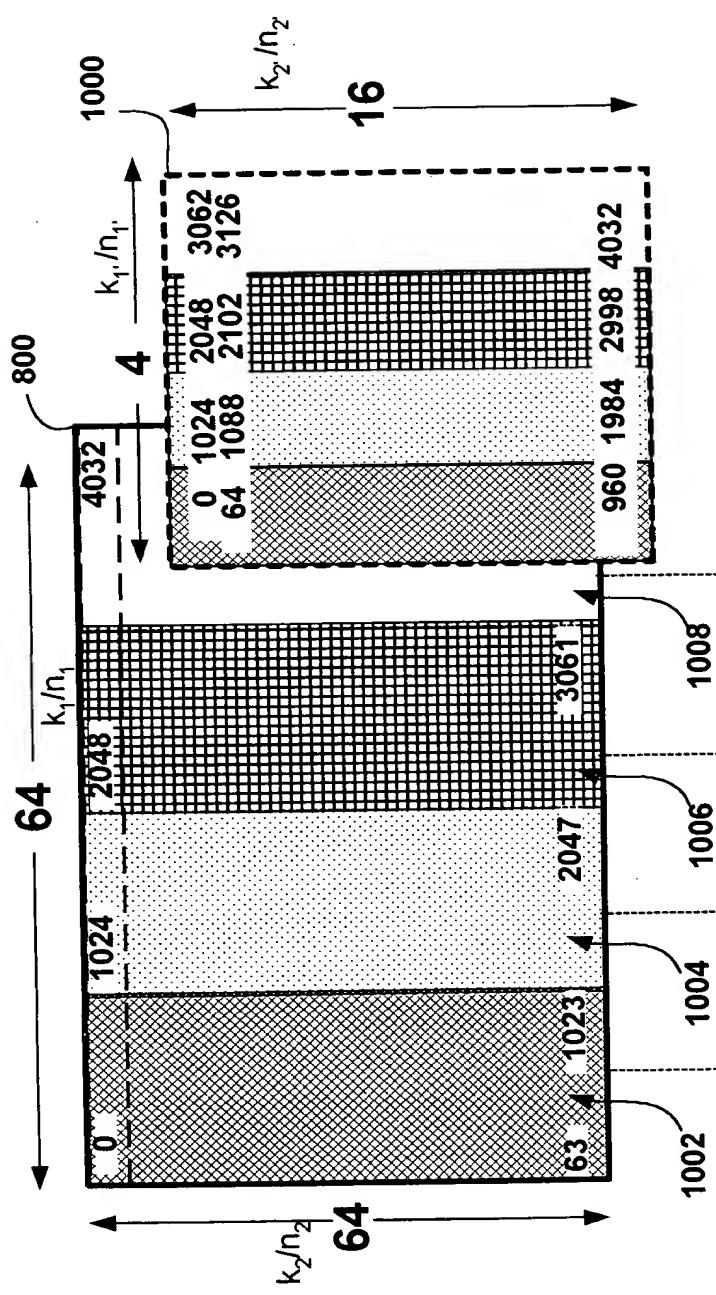


FIG. 10

FIG. 11A

TRANSMIT	G.LITE	ADSL	VDSL	Other
Sample space	256	512	512, 1024, 2048, 4096	
# Tones	128	256	256, 512, 1024, 2048	
Cyclic Prefix	16	32	Programmable	
Switching	● ●	● ●	● ●	
High Pass Filter	High Pass Filter	High Pass Filter	None	

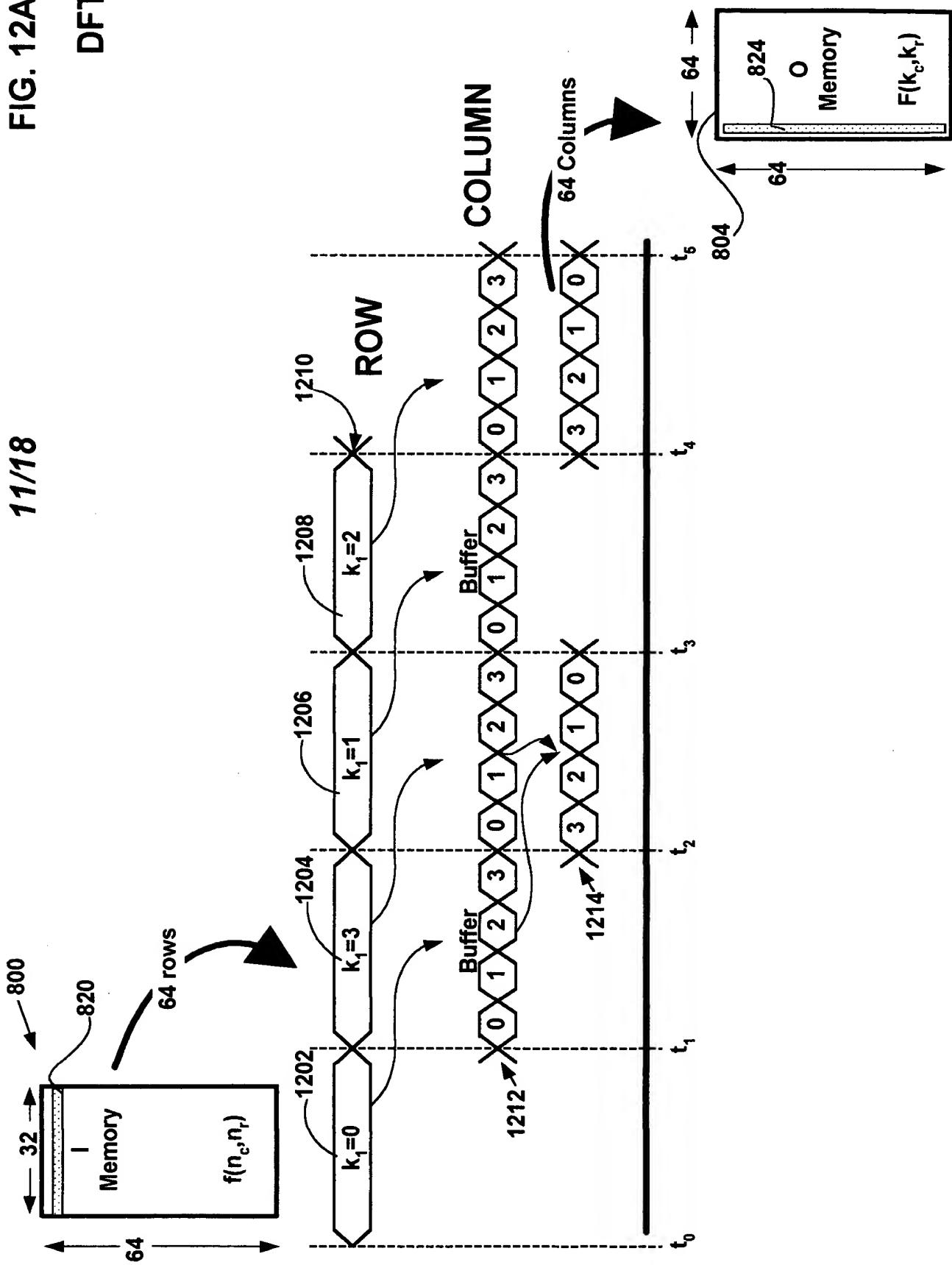
FIG. 11C

	Sample	ROW TRANSFORM	COLUMN TRANSFORM
4K	64	16	64
2K	32	8	64
1K	32	8	32
512	16	4	32
256	16	4	16
128	8	2	16
64	64	16	1

FIG. 11B

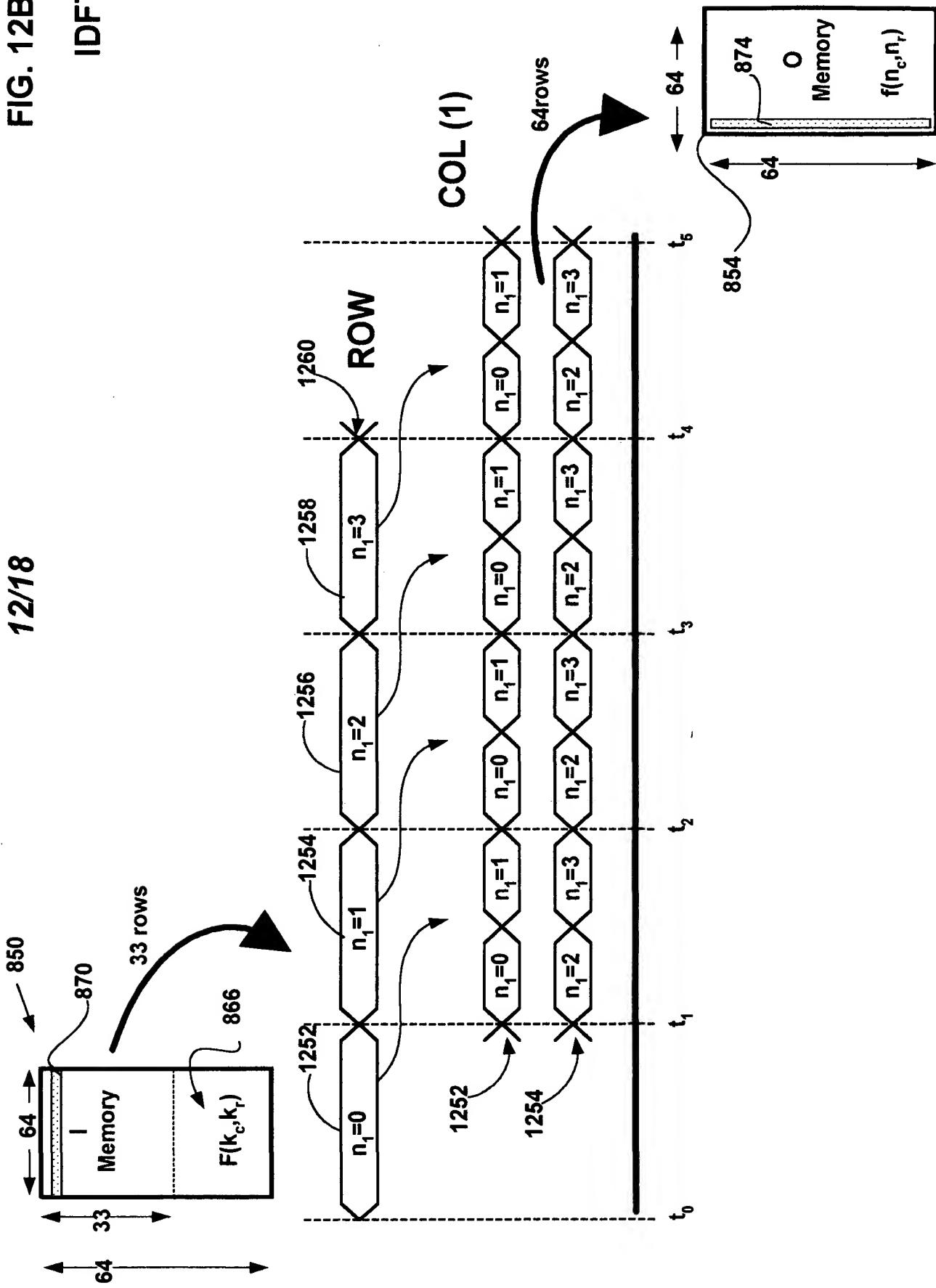
RECEIVE	G.LITE	ADSL	VDSL	Other
Sample space	64	64	512, 1024, 2048, 4096	
# Tones	32	32	256, 512, 1024, 2048	
Cyclic Prefix	4	4	Programmable	
Switching	● ●	● ●	● ●	

11/18

FIG. 12A
DFT

12/18

FIG. 12B **IDFT**



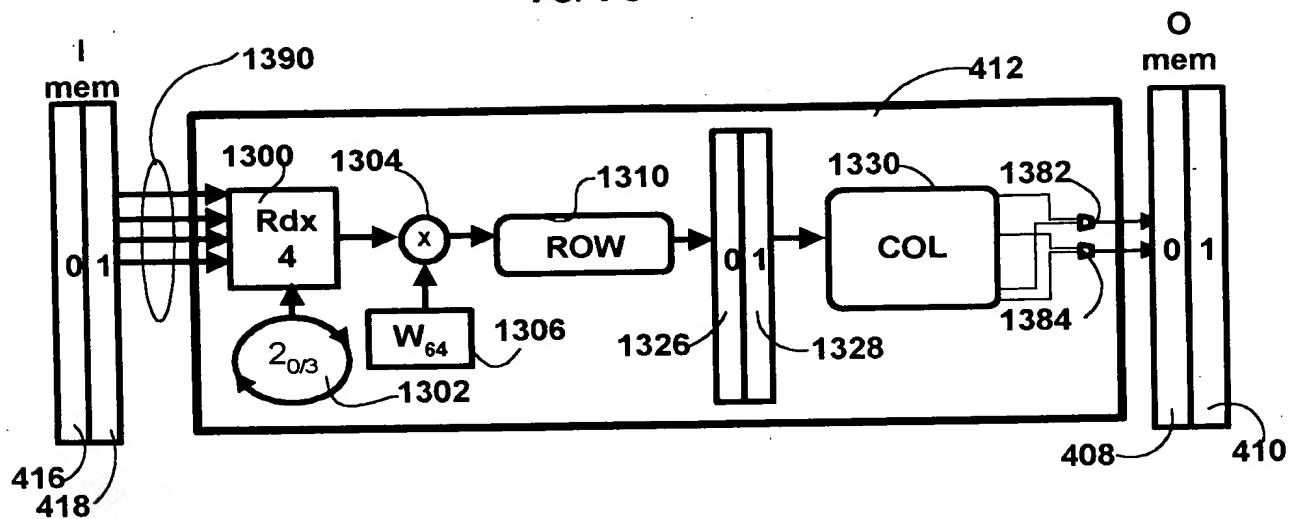


FIG. 13A

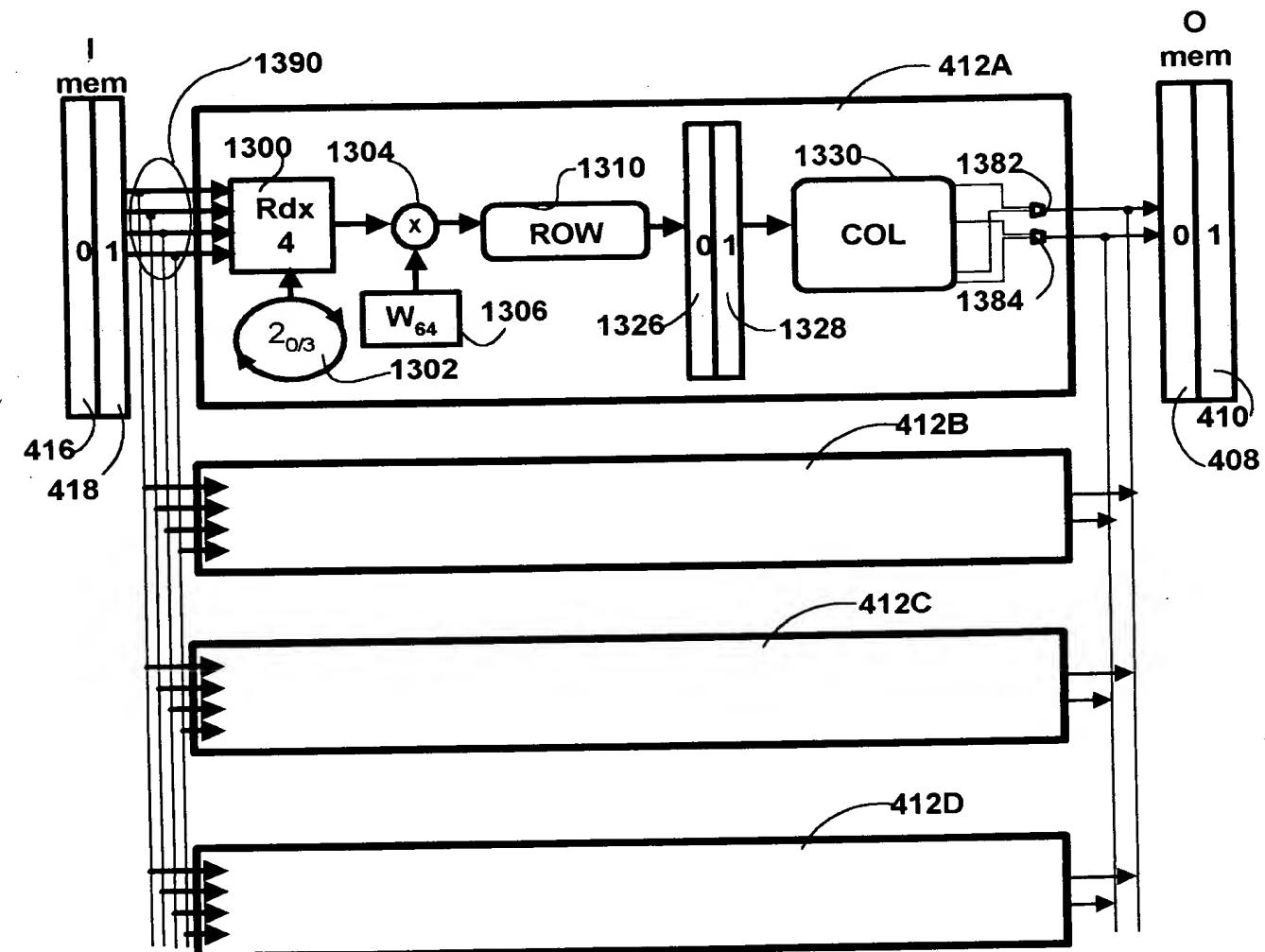


FIG. 13B

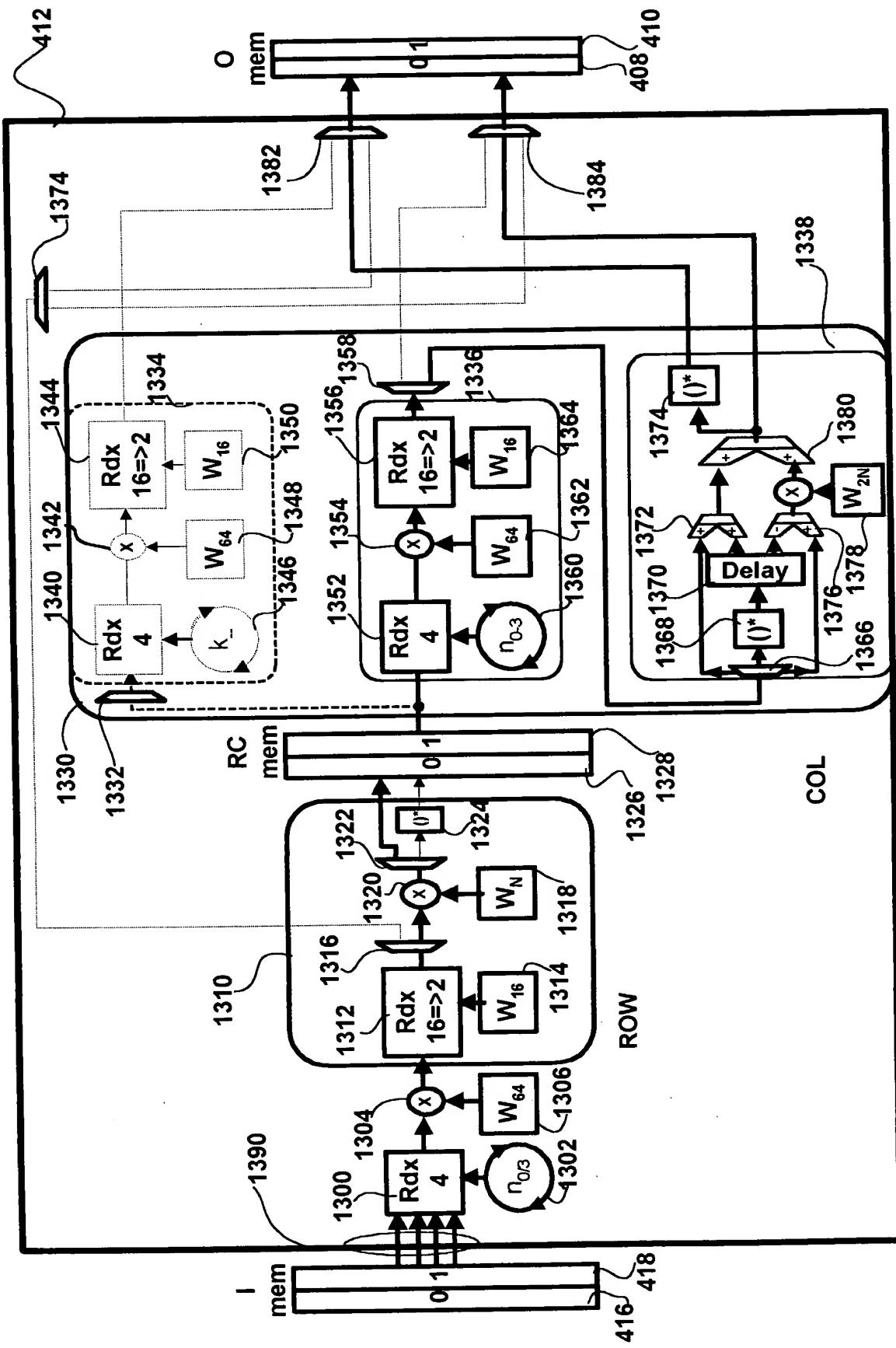


FIG. 13C

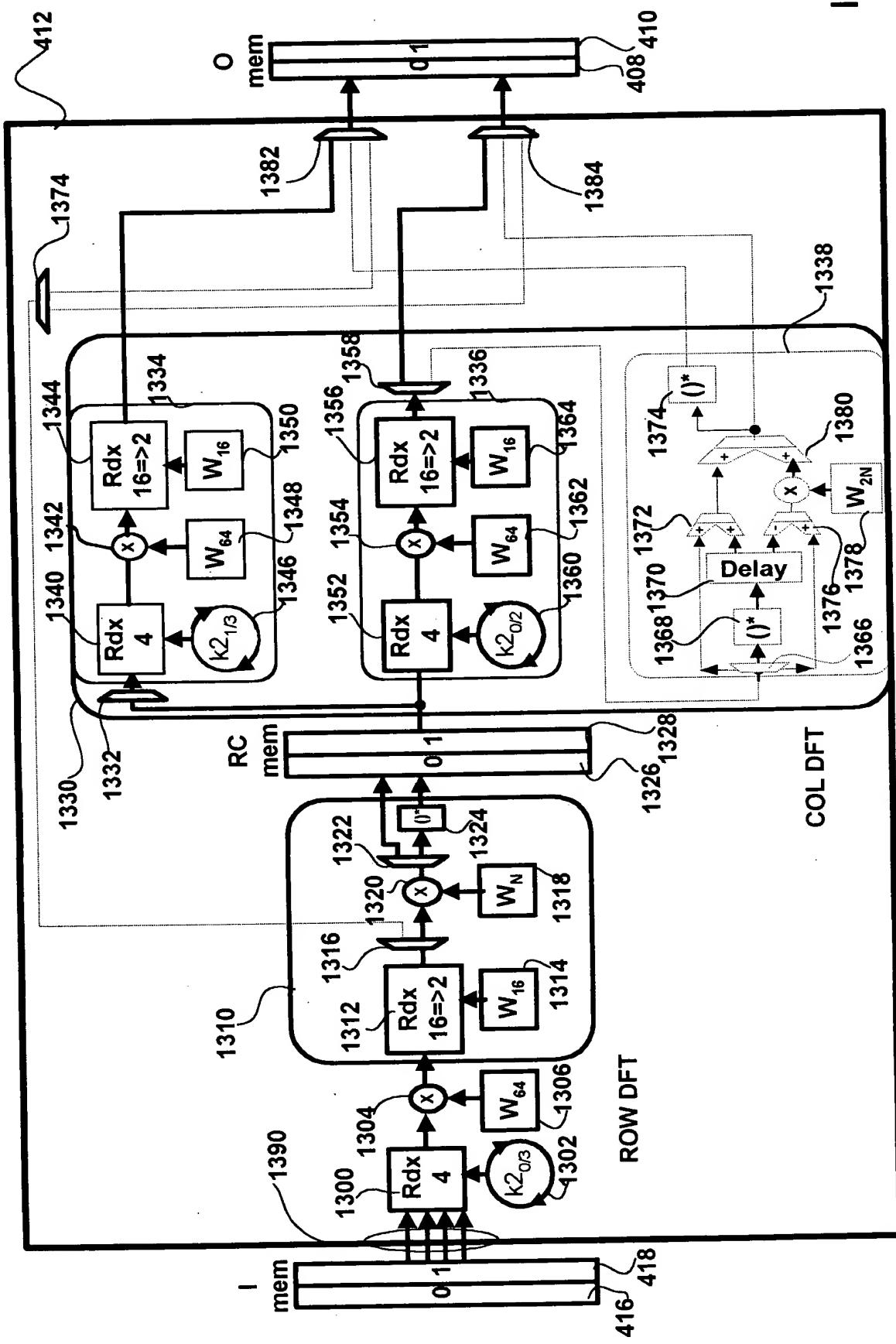
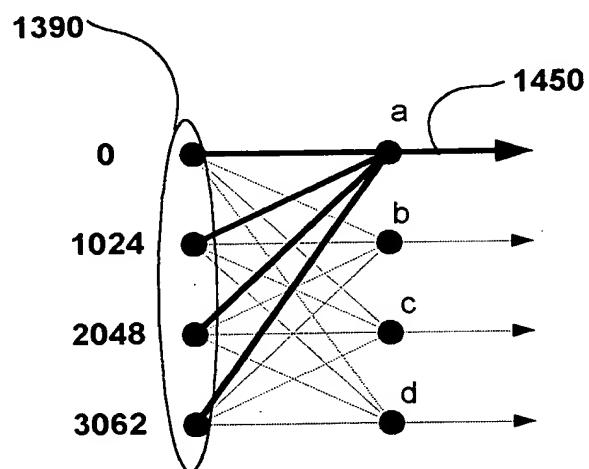
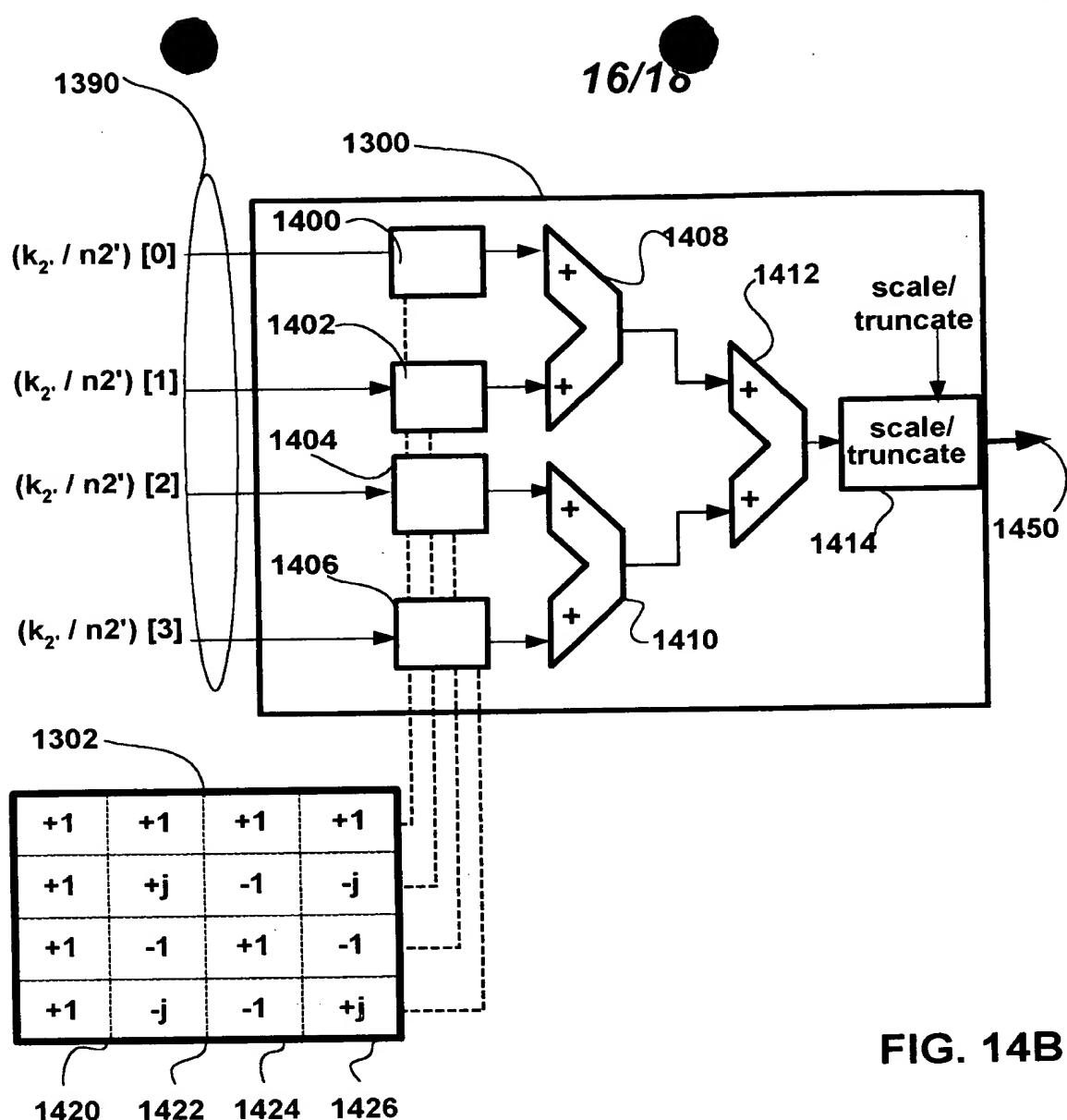


FIG. 13D



17/18

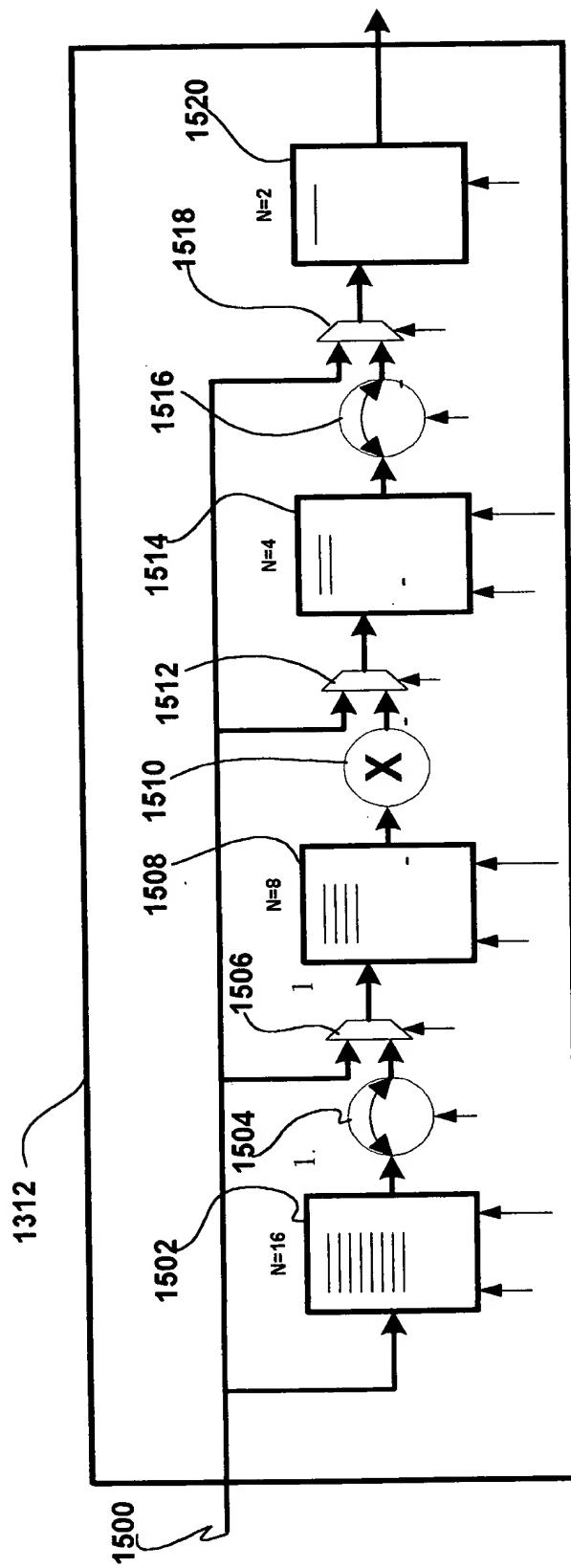


FIG. 15

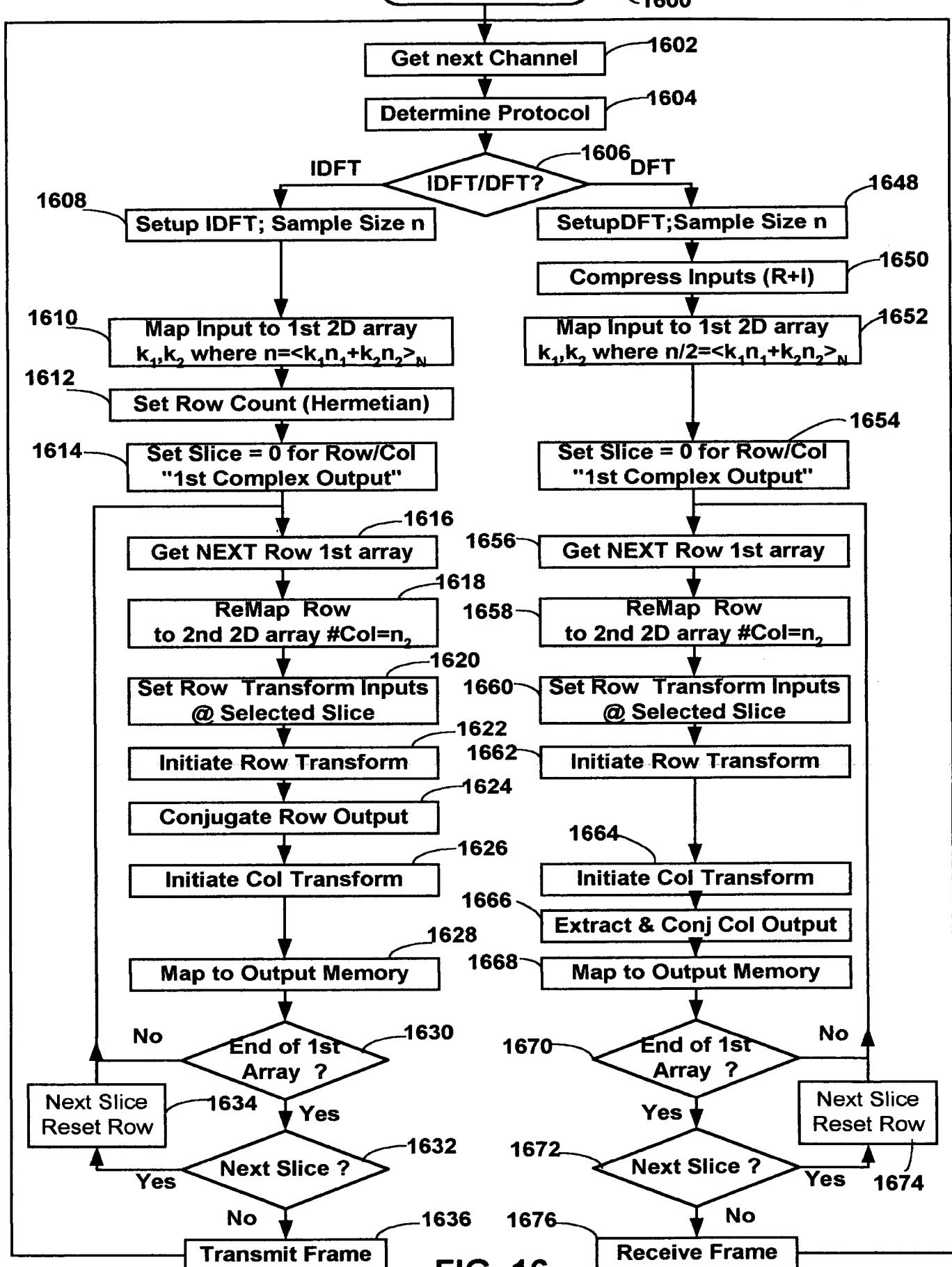


FIG. 16